

LOGIC.XLS

Logic Family	LOW Input max. (V)	High Input min. (V)	LOW Output max. (V)	HIGH Output min. (V)
TTL	0.80	2.00	0.40	2.40
CD4000	1.00	4.00	0.05	4.95
CD4000B	1.50	3.50	0.05	4.95
74C/54C	1.50	3.50	0.50	4.50
74HC/54HC	1.50	3.50	0.10	4.90
74HCT/54HCT	0.80	2.00	0.10	4.90
74HCU/54HCU	1.00	4.00	0.50	4.50
S TTL	0.80	2.00	0.50	2.50
LS TTL	0.70	2.00	0.40	2.50
AS TTL	0.80	2.00	0.50	3.00
ALS TTL	0.70	2.00	0.40	3.00
AC	1.50	3.50	0.10	4.90
ACT	0.80	2.00	0.10	4.90
74L/54L	0.70	2.00	0.30	2.40
Note: Supply at 5V/0V				

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